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**B.Sc. (ECS) – II (Semester – IV) (New CBCS Pattern) Examination, 2018  
PERIPHERALS & INTERFACING (Paper – VI)**

Day and Date : Friday, 11-5-2018  
Time : 10.30 a.m. to 1.00 p.m.

Max. Marks : 70

**Instructions :** 1) **All questions are compulsory.**  
2) **Figures to the right indicate full marks.**

1. Choose most correct alternative.

14

- 1) 8253 has \_\_\_\_\_ number of modes.  
a) 8                      b) 4                      c) 3                      d) 6
- 2) Instruction set of 8086 includes \_\_\_\_\_ instruction.  
a) 77                      b) 133                      c) 130                      d) 131
- 3) The 8051 has \_\_\_\_\_ parallel I/O ports.  
a) 2                      b) 3                      c) 4                      d) 5
- 4) STC is \_\_\_\_\_ instruction.  
a) logical                      b) data transfer  
c) processor control                      d) none of these
- 5) In linear decoding \_\_\_\_\_ line is used to generate chip select.  
a)  $A_{14}-A_{19}$                       b)  $A_{19}$                       c)  $A_{14}$                       d)  $A_{16}$
- 6) The internal RAM memory of the 8051 is \_\_\_\_\_ bytes.  
a) 32                      b) 64                      c) 128                      d) 256
- 7) The 8086 microprocessor has \_\_\_\_\_ byte instruction queue.  
a) 4                      b) 6                      c) 2                      d) 8
- 8) \_\_\_\_\_ is arithmetic instruction.  
a) AAA                      b) RCL                      c) STC                      d) MOV
- 9) 8255 has \_\_\_\_\_ number of I/O ports.  
a) 8                      b) 4                      c) 3                      d) 6
- 10) Virtual memory of 80386 is  
a) 64TB                      b) 64GB                      c) 64KB                      d) 4GB
- 11) 8257 is \_\_\_\_\_ device.  
a) PPI                      b) DMA                      c) PIC                      d) none of these

P.T.O.



- 12) \_\_\_\_\_ is the unconditional transfer instruction.  
a) JMP                      b) JC                      c) JNZ                      d) JNC
- 13) Pentium processor is introduced in \_\_\_\_\_  
a) 1992                      b) 1993                      c) 1991                      d) 1994
- 14) When pin no. 33 is connected to the  $V_{CC}$  the 8086 is operates in \_\_\_\_\_ mode.  
a) minimum                      b) maximum                      c) both a and b                      d) none of these

2. Attempt **any seven** of the following. **14**

- 1) Explain PUSH and POP instruction.
- 2) Explain AAA & DAA.
- 3) Give the features of 8086.
- 4) Explain shift instruction.
- 5) Explain BSR mode of 8255.
- 6) Explain multiplication instructions of 8086.
- 7) What are the difference between RISC and CISC.
- 8) Explain count register of 8257.
- 9) Give the features of 8051 microcontroller.

3. A) Attempt **any two** of the following. **10**

- 1) Explain bit manipulation instructions.
- 2) Explain memory mapped I/O.
- 3) Explain block diagram of 8257.

B) Explain absolute address decoding. **4**

4. Answer **any two** of the following. **14**

- 1) Explain with suitable diagram of maximum mode of 8086.
- 2) Explain architecture of microcontroller 8051.
- 3) Explain interfacing of LED display to microprocessor.

5. Answer **any two** of the following. **14**

- 1) Explain flag registers of microprocessor 8086.
- 2) Explain 8253 with suitable diagram.
- 3) Explain data transfer instruction with example.

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**Set P**